

Unit 1

Languages and Basic Notions

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a.a. 2019/2020

Guess what it is...



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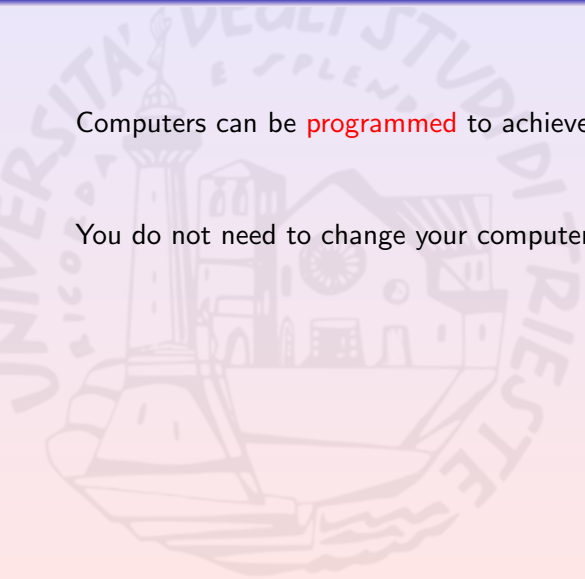


What is the main difference?

Programmability

Computers can be **programmed** to achieve new tasks.

You do not need to change your computer for any new task . . .



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Please, do **NOT** dry any cat in an oven!

What is a program?

As far as we are concerned:

It is a sequence of instructions that (hopefully) leads computers to perform an aimed task

What is a program?

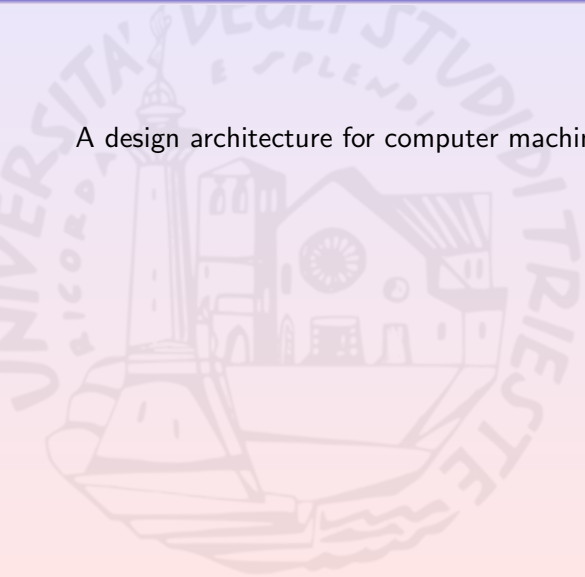
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So, what is a computer and what is an instruction?

von Neumann Architecture

A design architecture for computer machinery



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More or less, it consists in:

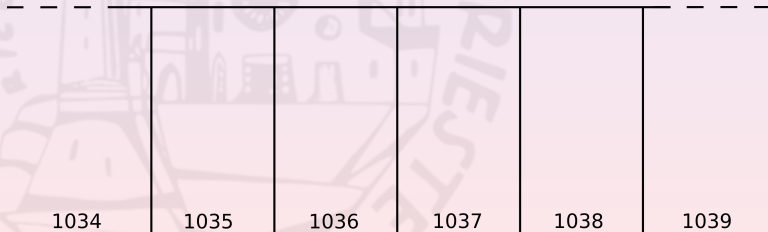
- a **central processing unit (CPU)**
- a **memory** storing both data and instructions
- I/O mechanisms

Virtually every electronic computer implements it

Memory

It is indexed by **addresses**

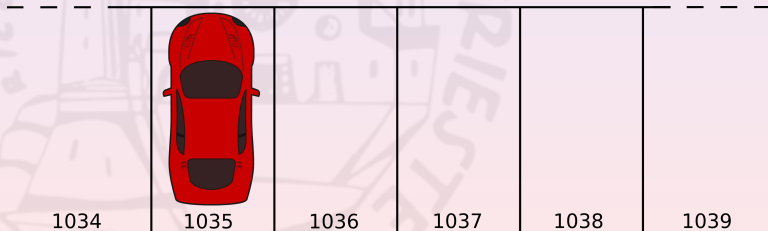
Just like ... parking stalls



Memory

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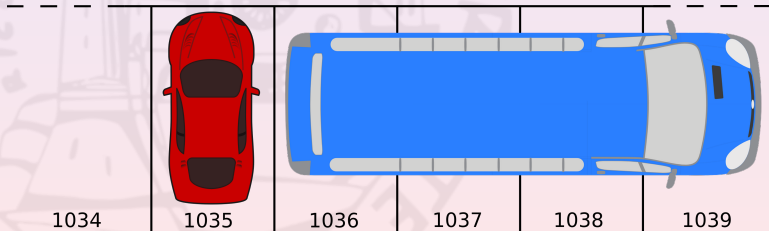
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Memory

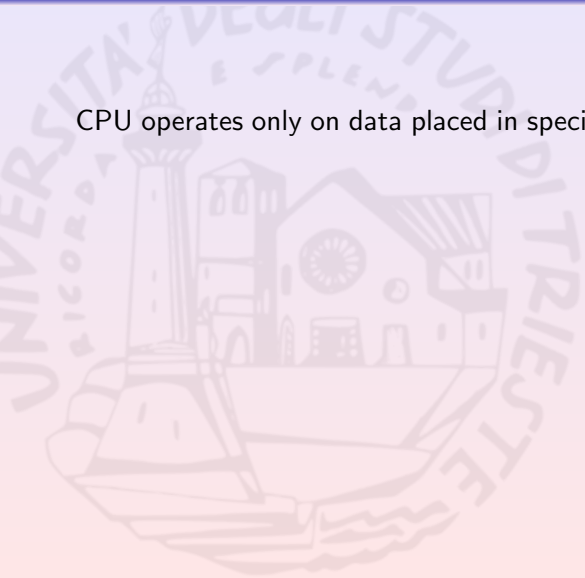
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Registers

CPU operates only on data placed in special locations: the **registers**



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E.g., To sum two numbers:

- load 1st number into R1
- load 2nd number into R2
- compute the sum
- save the result from R3 into the memory

Their names, number and usage depend on the CPU type.

How to represent data?

Digital computers store data as sequences of **B**inary **digiT**s (bits)

E.g.,

'a'
-3
13
27

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'a'	→	00011011
-3	→	11111101
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E.g.,

'a'	→	00011011
-3	→	11111101
13	→	00001101
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In 2019 the length of PC's registers is usually 64 bits (**64-bit architecture**).

How to represent instructions?

As bit sequences too

E.g., on Intel 4004:

Store 10 and 2 on R0 and R1

Load R0 on ACC

Sum R1 to ACC

Save ACC in R1

How to represent instructions?

As bit sequences too

E.g., on Intel 4004:

Store 10 and 2 on R0 and R1	→	0010 0000 1010 0010
Load R0 on ACC	→	1010 0000
Sum R1 to ACC	→	1000 0001
Save ACC in R1	→	1011 0001

Let's see whether you understood

Question

What bit sequence does assign 13 and 7 to R1 and R0?

Let's see whether you understood

Question

What bit sequence does assign 13 and 7 to R1 and R0?

Writing programs as bit sequences is:

- extremely difficult
- error prone
- CPU dependent

Programming Languages

Programming languages describe programs by using a human viable syntax.

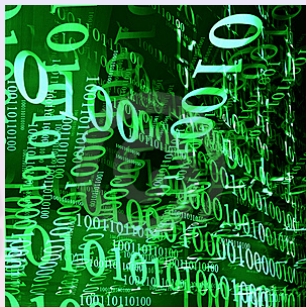
Syntax is simple (few rules) and semantics is not ambiguous (no sentences like *"I saw a man on a horse using a telescope"*).

We will focus on the **C** programming language.

Compilers

```
Image timer;  
Thread timer;  
public void init() {  
    lastcount = 10; count = 0;  
    pictures = new Image[10];  
    mediaTracker tracker = new Media  
    for (int a = 0; a < lastcount;  
        pictures[a] = getImage(  
            tracker.addImage(pictures  
            tracker.checkAll(true);  
    }  
    public void start() {  
        if (timer == null) {  
            timer = new Thread(  
                timer.start();  
        }  
    }  
    public void paint(Graphics  
        g.drawImage(picture  
        if (count == last  
    }  
    void run() {
```

Source code
(Sentence of the PL)



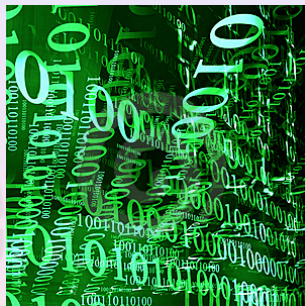
Bit sequences
(Machine code)

Compilers



```
image timer;  
Thread timer;  
public void init() {  
    lastcount = 10; count = 0;  
    pictures = new Image[10];  
    mediaTracker tracker = new MediaTracker(this);  
    for (int a = 0; a < lastcount; a++) {  
        pictures[a] = getImage(a);  
        tracker.addImage(pictures[a], 0, 0);  
    }  
    tracker.checkAll(true);  
}  
public void start() {  
    if (timer == null) {  
        timer = new Thread(this);  
        timer.start();  
    }  
}  
public void paint(Graphics g) {  
    g.drawImage(pictures[0], 0, 0, 100, 100, this);  
    if (count == lastcount) {  
        timer.run();  
    }  
}
```

Source code
(Sentence of the PL)



Bit sequences
(Machine code)

Compilers “translate” source code into binary executables

Different architectures



Every architecture represent data in “its own way”, ...

Different architectures

... every binary code can be executed exclusively on the architecture for which was compiled

Never mind: new architecture \Rightarrow new compilation

Interpreted Languages

Some languages avoid compilation (e.g., Python)

Interpreters = programs that interpret other program on request

Pros

- No compilation
- Same executables for every architecture
- Executables are human readable

Cons

- Interpreter needed
- Execution time
- Resources eager

Developing C Programs

We will need:

- a computer :-P
- a text editor
- a C compiler
- ...

Coming soon...

- User/HW abstraction layers
- operative systems
- what is POSIX?
- Ubuntu Linux
- working with Command-line User Interface (CUI)