Unit 1 Languages and Basic Notions

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Guess what it is...



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What is the main difference?

Programmability

Computers can be programmed to achieve new tasks.

You do not need to change your computer for any new task ...

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Programmability

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You do not need to change your computer for any new task \dots

just install a new program

Please, do NOT dry any cat in an oven!

What is a program?

As far as we are concerned

it is a <u>finite sequence of instructions</u> that (hopefully) leads computers to perform an aimed task in a finite amount of time

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So, what is a computer and what is an instruction?

von Neumann Architecture

A design architecture for computer machinery

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A design architecture for computer machinery

More or less, it consists in:

- a central processing unit (CPU)
- a memory storing both data and instructions
- I/O mechanisms

Nowadays, virtually every electronic device implements it

Memory

It is indexed by addresses

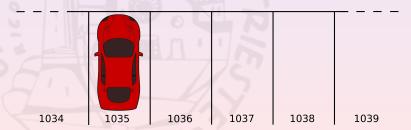
Just like ... parking stalls

			P.		
		-4			
1034	1035	1036	1037	1038	1039

Memory

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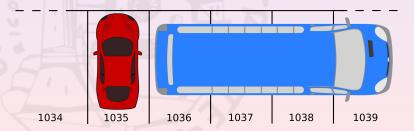
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Registers

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E.g., To sum two numbers:

- load 1st number into R1
- load 2nd number into R2
- evaluate the sum and store it in R3
- save the result from R3 into the memory

Their names, number and usage depend on the CPU type.

How to represent data?

Digital computers store data as sequences of Blnary digiTs (bits)

E.g.,

'a'

-3

13

27

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E.g.,

 $a' \rightarrow 00011011$

 $-3 \rightarrow 11111101$

 $13 \quad \rightarrow \quad 00001101$

 $27 \quad \rightarrow \quad 00011011$

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Digital computers store data as sequences of Blnary digiTs (bits)

E.g.,

$$\begin{array}{cccc} \mbox{`a'} & \rightarrow & 00011011 \\ -3 & \rightarrow & 11111101 \\ 13 & \rightarrow & 00001101 \\ 27 & \rightarrow & 00011011 \\ \end{array}$$

In 2021 the length of PC's registers is usually 64 bits (64-bit architecture).

How to represent instructions?

As bit sequences too

E.g., on Intel 4004 (the first microprocessor):

Store 10 and 2 in R1 and R0 Load R0 in ACC Sum R1 to ACC Save ACC in R1

How to represent instructions?

As bit sequences too

E.g., on Intel 4004 (the first microprocessor):

Store 10 and 2 in R1 and R0 0010 0000 1010 0010 Load R0 in ACC \rightarrow 1010 0000 \rightarrow 1000 0001 Sum R1 to ACC

Save ACC in R1 1011 0001

Let's see whether you understood

Question

What bit sequence does assign 13 and 7 to R1 and R0?

Let's see whether you understood

Question

What bit sequence does assign 13 and 7 to R1 and R0?

Writing programs as bit sequences is:

- extremely difficult
- error prone
- CPU dependent

Programming Languages

Programming languages describe programs by using a human viable syntax.

Syntax is simple (few rules) and semantics is not ambiguous (no sentences like "I saw a man on a horse using a telescope").

We will focus on the C programming language.

Compilers



Source code (PL senteces)



Bit sequences (Machine code)

Compilers



Source code (PL senteces)



Bit sequences (Machine code)

Compilers "translate" source code into binary executables

Different architectures



Every architecture represent data in "its own way", ...

Different architectures

... every binary code can be executed exclusively on the architecture for which was compiled

Never mind: new architecture \Rightarrow new compilation

Interpreted Languages

Some languages avoid compilation (e.g., Python)

Interpreters = programs that interpret other program on request

Pros

- No compilation
- Same executables for every architecture
- Executables are human readable

Cons

- Interpreter needed
- Execution time
- Resources eager

Developing C Programs

We will need:

- a computer :-P
- a text editor
- a C compiler
- . . .

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... but we are not exclusively interested in programming

Coming next...

- User/HW abstraction layers
- operating systems
- what is POSIX?
- Ubuntu Linux
- working with Command-line User Interface (CLI)